



Serial No. 10/717,214
Infineon Ref No. 13430
OC Ref No. I435.105.101

**DECLARATION FOR PATENT APPLICATION
& DESIGNATION OF CORRESPONDENCE ADDRESS**

As authorized by applicants-by-assignment, Infineon Technologies AG, I hereby declare that:

The inventors name, residence, post office address and citizenship are:

Javier Arguelles
Echardinger Str. 9,
81673 Munich, Germany

I believe Javier Arguelles, is the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

**PROCESS AND DEVICE FOR TESTING A SERIALIZER CIRCUIT ARRANGEMENT AND
PROCESS AND DEVICE FOR TESTING A DESERIALIZER CIRCUIT ARRANGEMENT**

the specification of which (check one)

_____ is attached hereto.

X was filed on 11/19/03 as Application Serial No. 10/717,214

_____ and was amended on _____ (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information known to me to be material to the patentability of this application in accordance with 37 CFR §1.56(a).

**PRIOR FOREIGN/PCT APPLICATION(S) FILED WITHIN 12 MONTHS (6 MONTHS FOR
DESIGN) PRIOR TO THIS APPLICATION AND ANY PRIORITY CLAIMS UNDER 35
U.S.C. § 119(a)-(d)**

I hereby claim foreign priority benefits under 35 USC § 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)			Priority Claimed	
Number	Country	Day/Month/Year Filed	Yes	No

CLAIM FOR BENEFIT OF PRIOR U.S. PROVISIONAL APPLICATION(S)
(35 U.S.C. § 119(e))

I hereby claim the benefit under Title 35, United States Code, § 119 (e) of any United States provisional application(s) listed below:

Provisional Application Number	Filing Date

CLAIM FOR BENEFIT OF EARLIER US/PCT APPLICATION(S)
UNDER 35 U.S.C. § 120

I hereby claim the benefits under 35 USC § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of 35 USC § 112, I acknowledge the duty to disclose material information as defined in 37 CFR § 1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

Appl. No.	Filing Date	Patented, Pending, Abandoned

Send correspondence to:

Paul P. Kempf
 Dicke, Billig & Czaja, PLLC
 Fifth Street Towers, Suite 2250
 100 South Fifth Street
 Minneapolis, MN 55402

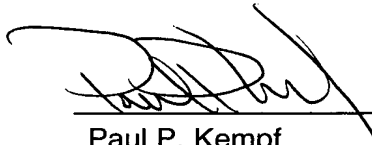
Direct telephone calls to:

Attorney (612) 767-2502

I hereby declare that all statements made herein on my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both under 18 USC §1001 and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Date:

June 30, 2004

A handwritten signature in black ink, appearing to read "Paul P. Kempf", is written over a horizontal line.

Paul P. Kempf
Attorney for Infineon Technologies AG
Applicant by Assignment
Reg. No.: 39,727



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Javier Argüelles	Examiner:	Unknown
Serial No.:	10/717,214	Group Art Unit:	Unknown
Filed:	November 19, 2003	Docket:	13430US/1435.105.101
Title:	PROCESS AND DEVICE FOR TESTING A SERIALIZER CIRCUIT ARRANGEMENT AND PROCESS AND DEVICE FOR TESTING A DESERIALIZER CIRCUIT ARRANGEMENT		

**STATEMENT OF FACTS IN SUPPORT OF FILING ON
BEHALF OF NON-SIGNING INVENTOR**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir/Madam:

This Statement is made as to the facts that are relied upon to establish the diligent effort made to secure the execution of the Declaration for Patent Application and Power of Attorney Document by the non-signing inventor for the above-identified patent application.

I presented non-signing inventor, Mr. Javier Argüelles, a copy of the above-identified application, including claims, drawings and oath or declaration. Non-signing inventor, Mr. Argüelles, indicated to me that he refused to sign the Declaration for Patent Application and Power of Attorney Document for the above-identified application, because he would prefer to include additional information to the application. However, because additional information is not needed to fully disclose the invention, it was not added to the application.

Non-signing inventor, Mr. Javier Argüelles did, however, assign the application to Infineon Technologies AG, and the executed Assignment is included with the present petition.

Furthermore, the invention that is detailed in the above-identified application is scheduled to be commercialized in early 2005. Consequently, if the application is not accorded a filing date rights will be lost and irreparable damage will be caused to Infineon Technologies AG.

BEST AVAILABLE COPY

Statement of Facts In Support of Filing on Behalf of Non-Signing Inventors

Applicant: Javier Argüelles

Serial No.: 10/717,214

Filed: November 19, 2003

Docket No.: 13430US/1435.104.101

Title: PROCESS AND DEVICE FOR TESTING A SERIALIZER CIRCUIT ARRANGEMENT AND PROCESS
AND DEVICE FOR TESTING A DESERIALIZER CIRCUIT ARRANGEMENT

This Statement is being made by the available person having first-hand knowledge of the facts recited above.

Dated: 04/06/28

A. Schaich

Axel Schaich

Senior Patent Counsel
Inficon Technologies AG